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THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Rick K. Dodge

§ Art Unit: 2818

Serial No.: 10/633,869

§ Examiner: Phuc T. Dang

Filed: August 4, 2003

§ Docket: ITO.0050US
P16247

For: Controlling the Location of
Conduction Breakdown in
Phase Change Memories

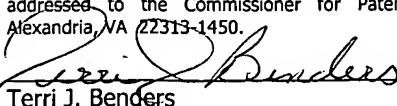
§ Assignee: Intel Corporation

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO PAPER NO. 0405

Sir:

In response to the Office action mailed April 27, 2005, please amend the above-referenced patent application as follows:

Date of Deposit: June 29, 2005
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Terri J. Benders